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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10017069	FILING DATE 12/13/2001	CLASS 361	SUBCLASS 100	GAU 2836	EXAMINER Laxton
<b>**APPLICANTS:</b> Perez Daniel;					
<b>**CONTINUING DATA VERIFIED:</b> None Cel					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> None Cel					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO 2019.312	
TITLE : Method and apparatus for preventing overload in a circuit					

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner		<b>CLAIMS ALLOWED</b>	
				Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner		<b>DRAWING</b>	
Amount Due	Date Paid			Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>		Application Examiner	
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